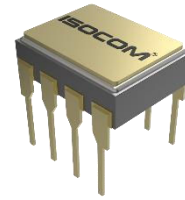


PART NUMBER

CS600/CS601

COMPONENT SPECIFICATION

ISSUE 5



Component Specification For Hermetically Sealed, Radiation-Hard High Gain Photon Optocouplers

Features	Applications
<ul style="list-style-type: none"> ▪ Total Ionizing Dose tested to 1 Mrad(si) ▪ Displacement Damage tested to 1 MEV x 10¹³ ▪ Hermetically Sealed ▪ High Withstand Test Voltage up to 1,500V_{DC} ▪ 8-pin DIP Package ▪ High Common Mode Rejection ▪ High Speed – typ 10 Mbits/s (75ns max) 	<ul style="list-style-type: none"> ▪ Space Radiation Equipment ▪ Military, high reliability system ▪ Medical instruments ▪ Mos, Cmos Applications ▪ Logic Interfacing ▪ Data Transmission ▪ Power Supply ▪ Modems

DESCRIPTION

These devices are single, hermetically sealed optically coupled isolators. Each channel is composed of a Gallium Arsenide infrared emitting diode coupled to an integrated high speed photon detector. The output of the detector is an open collector Schottky clamped transistor. These optocouplers have internal shield providing a guaranteed common mode transient immunity specification of 1,000 V/μs. These optocouplers are for Isolation Voltage applications requiring up to 2,500 V_{DC}.

The CS600 series is being used in environments encountered by space applications. Package styles for this device include 8-pin DIP package, with surface mount and solder dip options available.

Absolute maximum ratings, recommended operating conditions, electrical specifications and performance characteristics are identical for all units. Any exceptions, due to packaging variations and limitations, are as noted.



ISOCOM Limited is AS9100 certified for the design and manufacture of electronic and optoelectronic components.

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STANDARDS

The following specifications have been complied with in the manufacturing of this product -

Aerospace Compliance Standards

AS9100D & ISO 9001:2015 – Design & Manufacture of Electronic and Optoelectronic Components (*Ref GB15/92780*)

Military Compliance Specifications

MIL-PRF-38534 – General Specification for Hybrid Microcircuits

MIL-PRF-19500 – General Specification for Discrete Semiconductor Devices

Military Compliance Standards

MIL-STD-202 – Test Method Standard Electronic and Electrical Component Parts

MIL-STD-883 – Test Method Standard Microcircuits

MIL-STD-750 – Test Method Standard for Semiconductor Devices

SCREENING INFORMATION

Our products can be screened to MIL-PRF-38534, applying test methods from MIL-STD-883; MIL-PRF-19500, applying test methods of MIL-STD-750; or a combination thereof. Please contact us for more information relating to the applicable screening processes.

AMENDMENT RECORD

Issue No.	Date	Description
1	September 2013	First issue
2	May 2019	Screening and Group Test information removed, Format edited
3	May 2020	Pin numbers removed from schematic drawing
4	September 2020	Updated Quality Management Logos and removed IECQ Logos
5	November 2022	Updated Format

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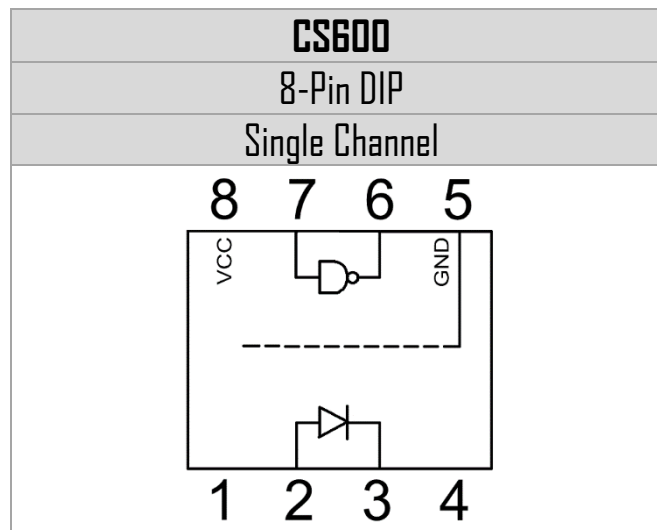
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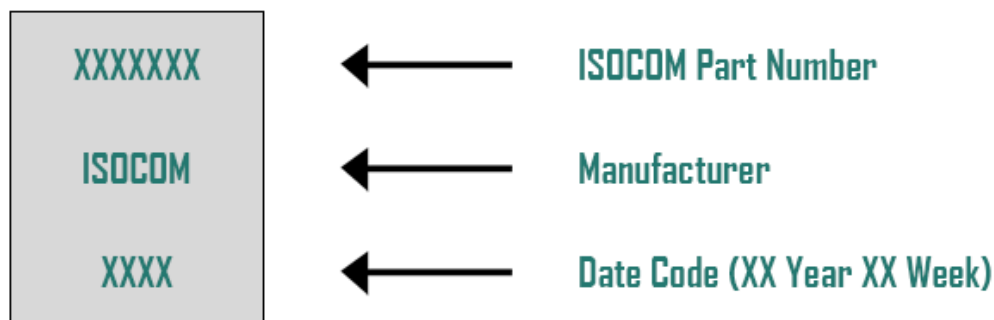
PACKAGE STYLES AND CONFIGURATION OPTIONS

Package	8-Pin DIP
Lead Style	-
Channels	1
Common Channel Wiring	-
Isocom Part Number and Options	
Commercial	CS600 CS601
Defense Screen Level	CS600/L2 CS601/L2
Space Screen Level	CS600/L2S CS601/L2S
Standard Gold Plate Finish	Gold Plate
Butt Joint	Option 10
Solder Dipped	Option 20
Gull Wing	Option 30
Crew Cut	Option 60

FUNCTIONAL DIAGRAMS



DEVICE MARKING



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ABSOLUTE MAXIMUM RATINGS

T_A = 25°C C.U.O.S

Parameter	Test Conditions
Storage Temperature	-65°C to +150°C
Operating Temperature	-55°C to +125°C
Lead Soldering Temperature	260°C 1.6mm from case for 10S
Input-to-Output Isolation Voltage	↑1500VDC
Input Diode	
Peak Forward Current	40mA ≤ 1 mS duration, 500pps
Average Forward Current	20mA (see note 3)
Reverse Voltage	5V
Power Dissipation	35mW
Output Detector	
Supply Voltage	7V > V _{cc} (1 minute maximum)
Current	25mA
Collector Power Dissipation	40mW
Voltage	7V > V _o (see note 1)

ELECTRICAL CHARACTERISTICS

T_A = 25°C C.U.O.S

*All typical Values at V_{cc} = 5V, T_A = 25°C each channel were appropriate

Parameter	Symbol	Test Conditions	Min	Typ.	Max	Units
Current Transfer Ratio ¹	CTR	V _{cc} =5.5V, V _o =0.6V, I _F = 10mA	100	-	-	%
Lower Level output voltage ^{1 & 9}	V _{OL}	V _{cc} =5.5V, I _F = 10mA, I _{oL} (sinking) 10mA	-	0.4	0.6	V
High level output current ¹	I _{oH}	I _F =250μA, V _o = V _{cc} =5.5V	-	20	250	μA
High level supply current	I _{CCH}	V _{cc} =5.5V, I _{F1} = I _{F2} = 0	-	15	28	mA
Low level supply current	I _{CCL}	V _{cc} =5.5V, I _{F1} = I _{F2} = 20mA	-	-	36	mA
Input forward voltage ¹	V _F	I _F = 10mA	-	1.5	1.9	V
		I _F = 20mA	-	-	1.9	V
Input-Output Insulation Leakage Current ^{2 & 10}	I _{I-O}	RH=45%, t=5S, V _{I-O} = 1500vdc	-	-	1.0	μA
Input reverse breakdown ^{1 & 6}	V _{BR}	I _R = 10μA	5	-	-	V
Propagation Delay Time to Low Output Level ^{1 & 6}	t _{PHL}	R _L = 510Ω, V _{cc} = 5V, I _F = 13mA, C _L = 50pF	-	-	100	nS
		R _L = 510Ω, V _{cc} = 5V, I _F = 13mA, C _L = 15pF	-	55	90	nS
Propagation Delay Time to High Output Level ^{1 & 5}	t _{PLH}	R _L = 510Ω, V _{cc} = 5V, I _F = 13mA, C _L = 50pF	-	-	100	nS
		R _L = 510Ω, V _{cc} = 5V, I _F = 13mA, C _L = 15pF	-	55	90	nS

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TYPICAL CHARACTERISTICS

$T_A = 25^\circ\text{C}$ U.O.S.

*All typical Values at $V_{CC} = 5\text{V}$, $T_A = 25^\circ\text{C}$ each channel where appropriate

Parameter	Symbol	Test Conditions	Min	Typ.	Max	Units
Resistance ³	R_{IO}	$V_{I0} = 500 V_{dc}$	-	10^{12}	-	Ω
Capacitance ³	C_{IO}	$f = 1\text{MHz}$	-	1.9	-	pF
Input Capacitance ¹	C_{IN}	$f = 1\text{MHz}$, $V_F = 0$	-	60	-	pF
Input Diode Temperature Coefficient ¹	$\frac{\Delta V_F}{\Delta T_A}$	$I_F = 20\text{mA}$	-	-1.9	-	mV/ $^\circ\text{C}$
Input-Input Insulation Leakage Current ⁴	I_{I-I}	45% Relative Humidity $V_{II} = 500 V_{dc}$, $t = 5\text{S}$,	-	0.5	-	nA
Resistance ⁴	R_{I-I}	$V_{II} = 500 V_{dc}$	-	10^{12}	-	Ω
Capacitance ⁴	C_{I-I}	$f = 1\text{MHz}$	-	0.6	-	pF
Output Rise (10-90%) ¹	tr	$R_L = 510\Omega$, $V_{CC} = 5\text{V}$, $I_F = 13\text{mA}$, $C_L = 15\text{pF}$	-	35	-	ns
Output Fall Time (90-10%) ¹	tf	$R_L = 510\Omega$, $V_{CC} = 5\text{V}$, $I_F = 13\text{mA}$, $C_L = 15\text{pF}$	-	35	-	ns
Common Mode Transient Immunity at Logic High Output Level ^{1&7}	CM_H	$V_O(\text{min}) = 2\text{V}$, $I_F = 0\text{mA}$, $V_{cm} = 10\text{V}$ (peak), $R_L = 510\Omega$	-	-1000	-	V/ μS
Common Mode Transient Immunity at Logic Low Output Level ^{1&7}	CM_L	$V_O(\text{max}) = 0.8\text{V}$, $I_F = 10\text{mA}$, $V_{cm} = 10\text{V}$ (peak), $R_L = 510\Omega$	-	-1000	-	V/ μS

Notes:

- Each channel, where appropriate.
- Measured between pins 1 through 4 shorted together, and pins 9 through 16 shorted together.
- Measured between pins 1 and 2, or 5 and 6 shorted together, and pins 9 through 16 shorted together.
- Measured between pins 1 and 2 shorted together, and pins 5 and 6 shorted together.
- The t_{PLH} propagation delay is measured from the 6.5mA point on the trailing edge of the input pulse to the 1.5V point on the trailing edge of the output pulse.
- The t_{PHL} propagation delay is measured from the 6.5mA point on the leading edge of the input pulse to the 1.5V point on the leading edge of the output pulse.
- CM_H is the maximum tolerable common mode transient to assure that the output will remain in a high logic state (i.e., $V_O > 2.0\text{V}$).
- CM_L is the maximum tolerable common mode transient to assure that the output will remain in the logic low state (i.e., $V_O < 2.0\text{V}$).
- It is essential that a bypass capacitor (0.1 to 0.1 μF , ceramic) be connected from pin 10 to pin 15. Total lead length between both ends of the capacitor and the isolator pins should not exceed 20mm.
- This is a momentary withstand test, not an operating condition.

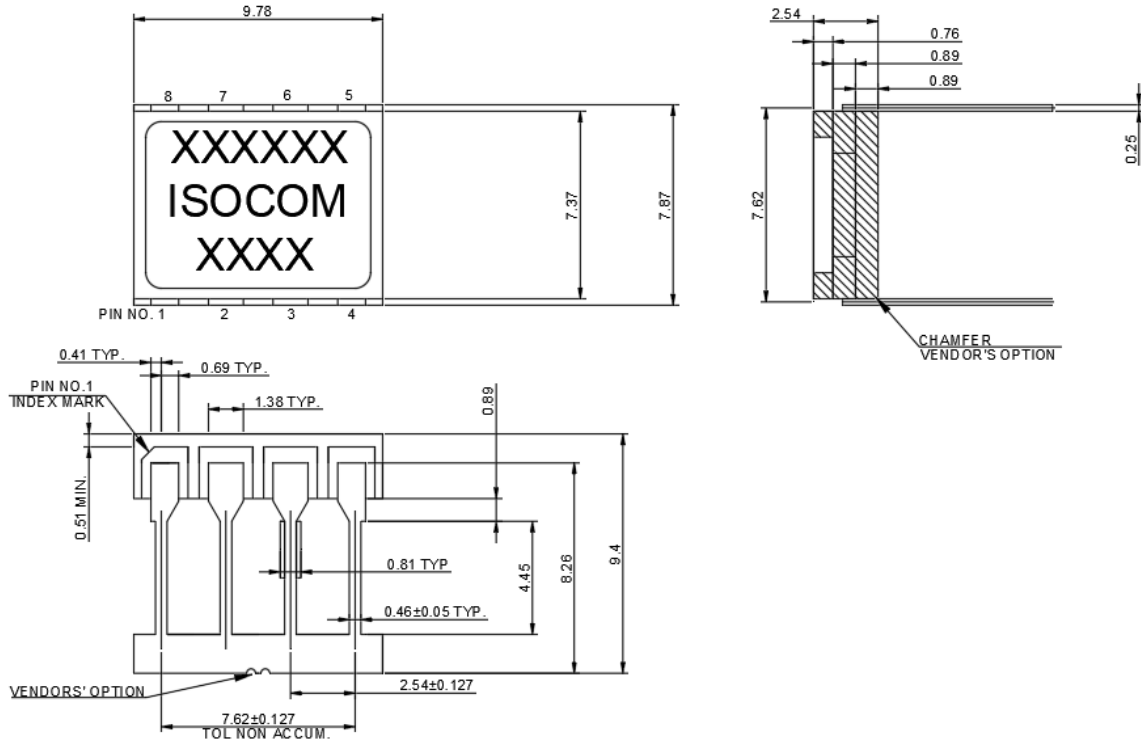
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OUTLINE DRAWINGS

8-Pin DIP



PIN OUT INFORMATION

Pin Number	Pin Function
1	N/C
2	LED Anode
3	LED Cathode
4	N/C
5	GND
6	Out
7	Enable
8	V _{CC}

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